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Sheet	1	of	1	Application Number	TBD
				Filing Date	Herewith
				First Named Inventor	Andrew W. Lueck
				Group Art Unit	TBD
				Examiner Name	Not Assigned
				Attorney Docket No.	TI-35560

<b>U.S. PATENT DOCUMENTS</b>					
Exam. Initials*	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)
		Number	Kind Code <sup>2</sup> (if known)		
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<b>FOREIGN PATENT DOCUMENTS</b>					
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<b>OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS</b>					
Exam. Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			
WJ	CA	PCI EXPRESS BASE SPECIFICATION, REV. 1.0, Isochronous Applications, pages 395-405.			
WJ	CB	PCI EXPRESS BASE SPECIFICATION, REV. 1.0, Virtual Channel Capability, pages 372-387.			
WJ	CC	PCI Express-SIG Developers Conference, Board Design Guidelines for PCI Express <sup>TM</sup> Interconnect, Intel Corporation, pages 1-4.			
WJ	CD	wysiwyg://raContent/raRightFrame.109/h.../hh/buses/1394-isoch_227b.asp?frame=true, MSDN FLASH NEWSLETTER, System Support for Buses: Windows DDK, Setting up Isochronous Transfer for IEEE 1394 Devices, July 24, 2002, pages 1-3			
WJ	CE	wysiwyg://raContent/raRightFrame.120/h.../hh/buses/1394-isoch_2kkn.asp?frame=true, MSDN FLASH NEWSLETTER, System Support for Buses: Windows DDK, Isochronous I/O for IEEE 1394 Devices, July 24, 2002, page 1.			
WA	CF	http://www.osr.com/ddk/buses/1394-isoch_74pz.htm, Completing an Isochronous Data Transfer, 05 September 2002, page 1.			
WJ	CG	wysiwyg://157/http://whatis.techtarget.com/definition/0,,sid9_gci748584,00.html, Whatis.com, IEEE 1394, Jun 18, 2001, pages 1-3.			
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Examiner Signature	Kim T. Nguyen	Date Considered	4/13/06
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